

FlashTool 0.2.9.1.exe [PATCHED]

; This section should be used in place of PLL1CONFIG when the I2C, SPI, or UART modes are being used. This ensures that the system PLL and the peripheral's clocks are changed together.; See PLL1CONFIG section for the format of the PLL1CFG fields.; See PERIPHCLKCFG section for the format of the CLKCFG field.; ----24 ----16 ----8 ----0; PLL1CFG0; PLL1CFG1; PLL1CFG2; PERIPHCLKCFG; CLKCFG; (PLLANDCLOCKCONFIG)PLL1CFG0 = 0x00000000;PLL1CFG1 = 0x00000000;PERIPHCLKCFG = 0x00000000; This section should be used in place of PLL2CONFIG when the I2C, SPI, or UART modes are being used. This ensures that the system PLL and the peripheral's clocks are changed together.; See PLL2CONFIG section for the format of the PLL2CFG fields.; See PERIPHCLKCFG section for the format of the CLKCFG field.; ----24 ----16 ----8 ----0; PLL2CFG0; PLL2CFG1; PLL2CFG2; PERIPHCLKCFG; CLKCFG; (PLLANDCLOCKCONFIG)PLL2CFG0 = 0x00000000;PLL2CFG1 = 0x00000000;PERIPHCLKCFG = 0x00000000; This section should be used in place of PLL3CONFIG when the I2C, SPI, or UART modes are being used. This ensures that the system PLL and the peripheral's clocks are changed together.; See PLL3CONFIG section for the format of the PLL3CFG fields.; See PERIPHCLKCFG section for the format of the CLKCFG field.; ----24 ----16 ----8 ----0; PLL3CFG0; PLL3CFG1; PLL3CFG2; PERIPHCLKCFG; CLKCFG; (PLLANDCLOCKCONFIG)PLL3CFG0 = 0x00000000;PLL3CFG1 = 0x00000000;PERIPHCLKCFG = 0x00000000

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